

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Andrew Graham et al.	Examiner:	Andreas Lopez Esquerra
Serial No.:	10/533,550	Group Art Unit:	2818
Filed:	November 17, 2005	Docket:	I432.116.101/P29858
Title:	VERTICALLY INTEGRATED FIELD-EFFECT TRANSISTOR		

REQUEST FOR CORRECTED FILING RECEIPT

Office of Initial Patent Examination's
Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby request correction of the Corrected Filing Receipt with respect to the above-identified patent application. In the Corrected Filing Receipt received in our office on June 6, 2008, the title as claimed by Applicants is incorrect.

In the Corrected Filing Receipt, the title *reads* as follows:

Vertically integrated field-effect transistor and method for fabricating

In the Corrected Filing Receipt, the title as claimed by Applicants and as amended by the Amendment and Response filed July 22, 2008 *should read* as follows:

Vertically integrated field-effect transistor

Applicants would appreciate the above-identified printing error be corrected and that a new "corrected" Filing Receipt be sent to Applicants' representatives at the address give below.

Respectfully submitted,

Andrew Graham et al.,

By their attorneys,

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Date: October 20, 2008

PPK:cmj

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